

Application No.: 09/736,335
Supplemental Amendment dated January 12, 2004

Docket No.: 8733.343.00-US

Listing of the Claims:

1. (Currently Amended) An in-plane switching mode liquid crystal display (LCD), comprising:

a first substrate having a switching element;

gate lines and data lines on the first substrate;

a second substrate;

a first electrode and a second electrode on the first substrate;

a common line in parallel to the gate lines and connected to the first electrode;

a transparent electrode asymmetrically overlapping a portion of the first electrode, the transparent electrode preventing vertical crosstalk caused by the data lines and the second electrode; and

a liquid crystal layer between the first substrate and the second substrate.

2. (Original) The in-plane switching mode LCD as claimed in claim 1, wherein the first electrode is a common electrode and the second electrode is a data electrode.

3. (Currently Amended) The in-plane switching mode LCD as claimed in claim 1, wherein the switching device element is a transistor including:

a gate electrode on the first substrate;

a gate insulating layer on the gate electrode and the first substrate;

a semiconductor layer on the gate insulating layer; and

source and drain electrodes on the semiconductor layer.

Application No.: 09/736,335
Supplemental Amendment dated January 12, 2004

Docket No.: 8733.343.00-US

4. (Currently Amended) The in-plane switching mode LCD as claimed in claim 1, wherein the switching ~~device~~ element is a transistor including a gate electrode, a source electrode and a drain electrode.
5. (Original) The in-plane switching mode LCD as claimed in claim 4, wherein the transistor includes a gate insulating film on the first substrate and the first electrode, the second electrode being formed on the gate insulating film.
6. (Original) The in-plane switching mode LCD as claimed in claim 5, wherein the gate insulating film is formed on the gate electrode and the gate electrode is on the same layer as the first electrode.
7. (Original) The in-plane switching mode LCD as claimed in claim 1, further comprising a protection film between the transparent electrode and the second electrode and wherein the first electrode, the second electrode and the transparent electrode are all on different layers.
8. (Original) The in-plane switching mode LCD as claimed in claim 1, further comprising a shielding electrode on the second substrate, the first and second electrodes and the shielding electrode forming a tilted electric field.
9. (Original) The in-plane switching mode LCD as claimed in claim 8, wherein the shielding electrode is a black matrix.
10. (Original) The in-plane switching mode LCD as claimed in claim 8, wherein the shielding electrode includes chrome(Cr).

Application No.: 09/736,335
Supplemental Amendment dated January 12, 2004

Docket No.: 8733.343.00-US

11. (Previously Presented) The in-plane switching mode LCD as claimed in claim 1, wherein the transparent electrode shields an electric field between the second electrode and the data line.
12. (Original) The in-plane switching mode LCD as claimed in claim 1, wherein the transparent electrode includes ITO.
13. (Original) The in-plane switching mode LCD as claimed in claim 1, wherein the first electrode has an outmost portion and the transparent electrode is asymmetrical with respect to the outermost portion of the first electrode.
14. (Currently Amended) An in-plane switching mode liquid crystal display comprising:
 - a first substrate having a switching element;
 - gate lines and data lines on the first substrate;
 - a second substrate;
 - a plurality of first electrodes including an outermost first electrode on the first substrate;
 - a common line in parallel to the gate lines and connected to the plurality of first electrodes;
 - a plurality of second electrodes on the first substrate;
 - a gate insulating film, a protection film, and a transparent film sequentially stacked on the outermost first electrode, wherein the transparent film at least partially covers the outermost first electrode and prevents vertical crosstalk caused by the data lines and the plurality of second electrodes; and
 - a liquid crystal layer between the first substrate and the second substrate.

Application No.: 09/736,335
Supplemental Amendment dated January 12, 2004

Docket No.: 8733.343.00-US

15. (Original) The in-plane switching mode LCD as claimed in claim 14, wherein the first electrodes and the second electrodes are on planes different from each other.
16. (Original) The in-plane switching mode LCD as claimed in claim 15, wherein the second electrodes are on the gate insulating film.
17. (Original) The in-plane switching mode LCD as claimed in claim 14, wherein the transparent film includes indium tin oxide (ITO).
18. (Original) The in-plane switching mode LCD as claimed in claim 14, further comprising a shielding electrode on the second substrate, wherein the shielding electrode forms a tilted electric field together with the first and second electrodes.
19. (Previously Presented) The in-plane switching mode LCD as claimed in claim 14, wherein the transparent film shields an electric field between the second electrode and the data line.
20. (Currently Amended) An in-plane switching mode liquid crystal display device comprising:
 - a first substrate having a switching element;
 - gate lines and data lines on the first substrate;
 - a second substrate;
 - a first electrode on the first substrate;
 - a common lines in parallel to the gate lines and connected to the first electrode;
 - a gate insulating film on an entire surface of the first substrate including the first electrode;

Application No.: 09/736,335

Docket No.: 8733.343.00-US

Supplemental Amendment dated January 12, 2004

a second electrode on the gate insulating film, the second electrode forming an in-plane electric field together with the first electrode;

a protection film on the first electrode and the second electrode;

an asymmetric transparent electrode on the protection film and overlapping a portion of the first electrode, the transparent electrode preventing vertical crosstalk caused by the data lines and the second electrode; and

a liquid crystal layer between the first substrate and the second substrate.

21. (Original) The in-plane switching mode liquid crystal display device as claimed in claim 20, further comprising a shielding electrode on the second substrate, wherein the shielding electrode forms a tilted electric field together with the first electrode and the second electrode.

22. (Original) The in-plane switching mode liquid crystal display device as claimed in claim 21, wherein the shielding electrode is a black matrix.

23. (Original) The in-plane switching mode liquid crystal display device as claimed in claim 21, wherein the black matrix is a Cr.

24. (Original) The in-plane switching mode liquid crystal display device as claimed in claim 20, wherein the first electrode is a common electrode and the second electrode is a data electrode.

25. (Original) The in-plane switching mode liquid crystal display device as claimed in claim 20, wherein the transparent electrode includes indium tin oxide (ITO).

Application No.: 09/736,335
Supplemental Amendment dated January 12, 2004

Docket No.: 8733.343.00-US

26. (Original) The in-plane switching mode liquid crystal display device as claimed in claim 20, wherein the common electrode has an outmost portion and the transparent electrode is on top of the outermost portion of the common electrode, the transparent electrode is asymmetrical.

27. (Currently Amended) An in-plane switching liquid crystal display (LCD) device comprising:

a first substrate and a second substrate;

gate lines and data lines on the first substrate;

a thin film transistor having a gate electrode, a source electrode and a drain electrode on the first substrate;

a liquid crystal material between the first and second substrate;

a common electrode on a first portion of the first substrate;

a common line in parallel to the gate lines and connected to the common electrode;

a data electrode on a second portion of the first substrate; and

a transparent electrode ~~over~~ overlapping a region of the first substrate, the region including at least a portion of the common electrode, the transparent electrode preventing vertical crosstalk caused by the data lines and the data electrode.

wherein the transparent electrode has a first part at a first height above the first substrate and a second part at a second height above the first ~~substrate-substrate.~~

28. (Cancelled)

29. (Original) The in-plane switching liquid crystal display device of claim 27, wherein the first part overlaps the common electrode and the second height does not overlap the common electrode.

Application No.: 09/736,335

Docket No.: 8733.343.00-US

Supplemental Amendment dated January 12, 2004

30. (Original) The in-plane switching liquid crystal display device of claim 29, wherein the first part of the transparent electrode is higher than the second part of the transparent electrode.
31. (Original) The in-plane switching liquid crystal display device of claim 27, further comprising a shielding electrode on the second substrate.
32. (Original) The in-plane switching liquid crystal display device of claim 31, wherein the shielding electrode is a black matrix.
33. (Original) The in-plane switching mode liquid crystal display as claimed in claim 32, wherein the black matrix is a Cr.
34. (Original) The in-plane switching mode liquid crystal display device as claimed in claim 27, wherein the transparent electrode includes indium tin oxide (ITO).
35. (Original) The in-plane switching mode liquid crystal display as claimed in claim 27, further comprising a protection film over the first substrate, including the common and data electrodes, the protection film being between the common electrode and the transparent electrode.
36. (Previously Presented) An in-plane switching liquid crystal display (LCD) device comprising:

a first substrate;

a second substrate;

a thin film transistor including:

a gate electrode on the first substrate;

Application No.: 09/736,335
Supplemental Amendment dated January 12, 2004

Docket No.: 8733.343.00-US

a gate insulating layer on the gate electrode;

a semiconductor layer on the gate insulating layer; and

a source electrode and a drain electrode on the semiconductor layer;

a gate line connected to the gate electrode extending in a first direction;

a data line connected to one of the source and drain electrodes extending in a second direction, the gate line and the data line defining a pixel region;

a common electrode on the first substrate on the same layer as the gate line and gate electrode and spaced from the gate electrode;

a common line in parallel to the gate lines and connected to the common electrode;

a data electrode connected to one of the source and drain electrodes on the gate insulating film and spaced from the common electrode;

a protection film on the thin film transistor;

a field distorting electrode on the protection film overlapping at least a portion of the common electrode, the field distorting electrode preventing vertical crosstalk caused by the data line and the data electrode;

a black matrix on the second substrate; and

a liquid crystal material between the first and second orientation films[.],

wherein the field distorting electrode includes first and second portions.

37. (Cancelled)

38. (Previously Presented) The in-plane switching liquid crystal display device as claimed in claim 36, wherein the first portion overlaps the common electrode and the second portion does not overlap the common electrode.

Application No.: 09/736,335

Docket No.: 8733.343.00-US

Supplemental Amendment dated January 12, 2004

39. (Previously Presented) The in-plane switching liquid crystal display device as claimed in claim 36, wherein the first portion is above the second portion.

40. (Original) The in-plane switching liquid crystal display device as claimed in claim 36, wherein the field-distorting electrode includes a transparent conductive material.

41. (Original) The in-plane switching liquid crystal display device as claimed in claim 40, wherein the transparent conductive material includes indium tin oxide.

42. (Original) The in-plane switching liquid crystal display device as claimed in claim 36, wherein the field distorting electrode forms an electric field with the data line, the data electrode and the black matrix.

43. (Original) The in-plane switching liquid crystal display device as claimed in claim 36, wherein the protection film covers the common electrode and the data electrode.

44. (Original) The in-plane switching liquid crystal display device as claimed in claim 36, further comprising a first orientation film on the protection film and the field distorting electrode.

45. (Original) The in-plane switching liquid crystal display device as claimed in claim 44, further comprising:

a color filter layer on the second substrate; and

a second orientation film on the color filter layer.

Application No.: 09/736,335

Docket No.: 8733.343.00-US

Supplemental Amendment dated January 12, 2004

46. (Original) The in-plane switching liquid crystal display device as claimed in claim 44, further comprising an overcoat layer between the color filter layer and the second orientation film.

47. (Original) The in-plane switching liquid crystal display device as claimed in claim 36, the field distorting electrode shields an electric field between the data line and the data electrode.

48. (Previously Presented) A method of manufacturing an in-plane switching liquid crystal display (LCD) device comprising:

forming a thin film transistor including:

forming a gate electrode on a first substrate;

forming a gate insulating layer on the gate electrode;

forming a semiconductor layer on the gate insulating layer; and

forming a source electrode and a drain electrode on the semiconductor layer;

forming a gate line connected to the gate electrode extending in a first direction;

forming a data line connected to one of the source and drain electrodes extending in a second direction, the gate line and the data line defining a pixel region;

forming a common electrode on the first substrate on the same layer as the gate line and gate electrode and spaced from the gate electrode;

forming a common line in parallel with the gate lines and connected to the common electrode;

forming a data electrode connected to one of the source and drain electrodes on the gate insulating film and spaced from the common electrode;

Application No.: 09/736,335
Supplemental Amendment dated January 12, 2004

Docket No.: 8733.343.00-US

forming a protection film on the thin film transistor, the common electrode and the data electrode;

forming a field distorting electrode on the protection film overlapping at least a portion of the common electrode, the field distorting electrode preventing vertical crosstalk caused by the data line and the data electrode; and

forming a first orientation film on the protection film and the field-distorting electrode, wherein the field distorting electrode includes first and second portions.

49. (Original) The method as claimed in claim 48, further comprising:

forming a black matrix on a second substrate;

forming a color filter layer on the second substrate;

forming a second orientation film on the color filter layer; and

forming a liquid crystal material between the first and second orientation films.

50. (Cancelled)

51. (Previously Presented) The method as claimed in claim 48, wherein the first portion overlaps the common electrode and the second portion does not overlap the common electrode.

52. (Currently Amended) The method as claimed in claim 48, wherein the first portion is above the second portion.

53. (Original) The method as claimed in claim 48, wherein the field-distorting electrode includes a transparent conductive material.

54. (Original) The method as claimed in claim 52, wherein the transparent conductive material includes indium tin oxide.

Application No.: 09/736,335

Docket No.: 8733.343.00-US

Supplemental Amendment dated January 12, 2004

55. (Original) The method as claimed in claim 48, wherein the field distorting electrode forms an electric field with the data line, the data electrode and the black matrix.

56. (Original) The method as claimed in claim 48, wherein the field distorting electrode shields an electric field between the data line and the data electrode.